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PLUS Search Results for S/N 09966889, Searched September 02, 2004

The Patent Linguistics Utility System (PLUS) is a USPTO automated search system for U.S. Patents from 1971 to the present. PLUS is a query-by-example search system which produces a list of patents that are most closely related linguistically to the application searched. This search was prepared by the staff of the Scientific and Technical Information Center, SIRA.

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09966889\_CLS

Most Frequently Occurring Classifications of Patents Returned  
From A Search of 09966889 on September 02, 2004

Original Classifications

9	710/315
4	710/306
2	710/100
2	710/107
2	710/260
2	710/307
2	710/310
2	710/311

Cross-Reference Classifications

4	710/100
4	710/107
3	370/402
3	710/105
3	710/113
3	710/311
2	307/98
2	326/30
2	710/22
2	710/312
2	710/315
2	711/163

Combined Classifications

11	710/315
6	710/100
6	710/107
5	710/306
5	710/311
3	370/402
3	710/105
3	710/113
3	710/22
3	710/260
3	710/307
3	710/312
2	307/98
2	326/30
2	710/104
2	710/108
2	710/112
2	710/310
2	710/313

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2 710/5  
2 710/8  
2 711/163

09966889\_CLSTITLES

Titles of Most Frequently Occurring Classifications of Patents Returned

From A Search of 09966889 on September 02, 2004

11	710/315	(9 OR, 2 XR)
	Class 710 :	ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
	710/100	INTRASYSTEM CONNECTION (E.G., BUS AND BUS TRANSACTION PROCESSING)
	710/305	.Bus interface architecture
	710/306	..Bus bridge
	710/315	...Different protocol (e.g., PCI to ISA)
6	710/100	(2 OR, 4 XR)
	Class 710 :	ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
	710/100	INTRASYSTEM CONNECTION (E.G., BUS AND BUS TRANSACTION PROCESSING)
6	710/107	(2 OR, 4 XR)
	Class 710 :	ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
	710/100	INTRASYSTEM CONNECTION (E.G., BUS AND BUS TRANSACTION PROCESSING)
	710/107	.Bus access regulation
5	710/306	(4 OR, 1 XR)
	Class 710 :	ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
	710/100	INTRASYSTEM CONNECTION (E.G., BUS AND BUS TRANSACTION PROCESSING)
	710/305	.Bus interface architecture
	710/306	..Bus bridge
5	710/311	(2 OR, 3 XR)
	Class 710 :	ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
	710/100	INTRASYSTEM CONNECTION (E.G., BUS AND BUS TRANSACTION PROCESSING)
	710/305	.Bus interface architecture
	710/306	..Bus bridge
	710/311	...Intelligent bridge
3	370/402	(0 OR, 3 XR)
	Class 370 :	MULTIPLEX COMMUNICATIONS
	370/351	PATHFINDING OR ROUTING
	370/389	.Switching a message which includes an address



## 09966889\_CLSTITLES

header

- 370/400 ..Having a plurality of nodes performing distributed switching
  - 370/401 ...Bridge or gateway between networks
  - 370/402 ....Bridge between bus systems
- 
- 3 710/105 (0 OR, 3 XR)
    - Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
    - 710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS TRANSACTION PROCESSING)
    - 710/105 .Protocol
- 
- 3 710/113 (0 OR, 3 XR)
    - Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
    - 710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS TRANSACTION PROCESSING)
    - 710/107 .Bus access regulation
    - 710/113 ..Centralized bus arbitration
- 
- 3 710/22 (1 OR, 2 XR)
    - Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
    - 710/1 INPUT/OUTPUT DATA PROCESSING
    - 710/22 .Direct Memory Accessing (DMA)
- 
- 3 710/260 (2 OR, 1 XR)
    - Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
    - 710/260 INTERRUPT PROCESSING
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- 3 710/307 (2 OR, 1 XR)
    - Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
    - 710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS TRANSACTION PROCESSING)
    - 710/305 .Bus interface architecture
    - 710/306 ..Bus bridge
    - 710/307 ...Variable or multiple bus width
- 
- 3 710/312 (1 OR, 2 XR)
    - Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
    - 710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS TRANSACTION PROCESSING)
    - 710/305 .Bus interface architecture
    - 710/306 ..Bus bridge

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710/312           ...Multiple bridges

2   307/98           (0 OR, 2 XR)  
       Class   307 :   ELECTRICAL TRANSMISSION OR INTERCONNECTION  
                       SYSTEMS  
       307/98           COMBINED IMPEDANCE AND SWITCH SYSTEMS

2   326/30           (0 OR, 2 XR)  
       Class   326 :   ELECTRONIC DIGITAL LOGIC CIRCUITRY  
       326/21           SIGNAL SENSITIVITY OR TRANSMISSION INTEGRITY  
       326/30           .Bus or line termination (e.g., clamping,  
                       impedance matching, etc.)

2   710/104          (1 OR, 1 XR)  
       Class   710 :   ELECTRICAL COMPUTERS AND DIGITAL DATA  
                       PROCESSING SYSTEMS: INPUT/OUTPUT  
       710/100          INTRASYSTEM CONNECTION (E.G., BUS AND BUS  
                       TRANSACTION PROCESSING)  
       710/104          .System configuring

2   710/108          (1 OR, 1 XR)  
       Class   710 :   ELECTRICAL COMPUTERS AND DIGITAL DATA  
                       PROCESSING SYSTEMS: INPUT/OUTPUT  
       710/100          INTRASYSTEM CONNECTION (E.G., BUS AND BUS  
                       TRANSACTION PROCESSING)  
       710/107          .Bus access regulation  
       710/108          ..Bus locking

2   710/112          (1 OR, 1 XR)  
       Class   710 :   ELECTRICAL COMPUTERS AND DIGITAL DATA  
                       PROCESSING SYSTEMS: INPUT/OUTPUT  
       710/100          INTRASYSTEM CONNECTION (E.G., BUS AND BUS  
                       TRANSACTION PROCESSING)  
       710/107          .Bus access regulation  
       710/112          ..Bus request queuing

2   710/310          (2 OR, 0 XR)  
       Class   710 :   ELECTRICAL COMPUTERS AND DIGITAL DATA  
                       PROCESSING SYSTEMS: INPUT/OUTPUT  
       710/100          INTRASYSTEM CONNECTION (E.G., BUS AND BUS  
                       TRANSACTION PROCESSING)  
       710/305          .Bus interface architecture  
       710/306          ..Bus bridge  
       710/310          ...Buffer or que control

2   710/313          (1 OR, 1 XR)  
       Class   710 :   ELECTRICAL COMPUTERS AND DIGITAL DATA  
                       PROCESSING SYSTEMS: INPUT/OUTPUT

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710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS TRANSACTION PROCESSING)

710/305 .Bus interface architecture

710/306 ..Bus bridge

710/313 ...Peripheral bus coupling (e.g., PCI, USB, ISA, and etc.)

2 710/5 (1 OR, 1 XR)

Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT

710/1 INPUT/OUTPUT DATA PROCESSING

710/5 .Input/Output command process

2 710/8 (1 OR, 1 XR)

Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT

710/1 INPUT/OUTPUT DATA PROCESSING

710/8 .Peripheral configuration

2 711/163 (0 OR, 2 XR)

Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY

711/100 STORAGE ACCESSING AND CONTROL

711/154 .Control technique

711/163 ..Access limiting